

HB32

March 28, 2001

Re: **09/328,910 (US 5,637,913)**

Dear Examiner Clark,

Attached are the requested litigation search results.

Several patent litigation and news databases were searched using Dialog, Lexis-Nexis and Questel-Orbit search systems.

No litigation information was found.

Thank you for using EIC2800!

Paul Schulwitz  
308-6559  
CP4-9C18

1/39/1

DIALOG(R) File 345:Inpadoc/Fam.& Legal Stat  
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11916710

Basic Patent (No,Kind,Date): JP 6216303 A2 19940805 <No. of Patents: 009>

Patent Family:

Patent No	Kind	Date	Applic No	Kind	Date	
JP 6216303	A2	19940805	JP 9365784	A	19930325	(BASIC)
JP 10326859	A2	19981208	JP 98185261	A	19980630	
JP 11224928	A2	19990817	JP 98333269	A	19980101	
JP 11224929	A2	19990817	JP 98333270	A	19980101	
JP 11224930	A2	19990817	JP 98333271	A	19980101	
JP 2000286374	A2	20001013	JP 20002000073	A	20000101	
JP 2891607	B2	19990517	JP 9365784	A	19930325	
US 5378656	A	19950103	US 38684	A	19930329	
US 5637913	A	19970610	US 311021	A	19940922	

Priority Data (No,Kind,Date):

JP 9365784 A 19930325  
JP 9271116 A 19920327  
JP 92320098 A 19921130  
JP 98185261 A 19980630  
US 311021 A 19940922  
US 38684 A3 19930329

PATENT FAMILY:

JAPAN (JP)

Patent (No,Kind,Date): JP 6216303 A2 19940805

LEAD FRAME AND ITS MANUFACTURE, AND MANUFACTURE OF SEMICONDUCTOR  
INTEGRATED CIRCUIT USING IT (English)

Patent Assignee: HITACHI LTD; HITACHI MICOM SYST KK

Author (Inventor): KAJIWARA YUJIRO; SUZUKI KAZUNARI; TSUBOSAKI  
KUNIHIO; SUZUKI HIROMICHI; MIYAKI YOSHINORI; NAITO TAKAHIRO; KAWAI  
SUEO

Priority (No,Kind,Date): JP 9365784 A 19930325; JP 9271116 A  
19920327; JP 92320098 A 19921130

Applic (No,Kind,Date): JP 9365784 A 19930325

IPC: \* H01L-023/50

Derwent WPI Acc No: \* C 94-288930; C 94-288930

JAPIO Reference No: \* 180577E000032; 180577E000032

Language of Document: Japanese

Patent (No,Kind,Date): JP 10326859 A2 19981208

LEAD FRAME AND SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE USING THE SAME  
(English)

Patent Assignee: HITACHI LTD; HITACHI MICROCOMPUTER SYST

Author (Inventor): KAJIWARA YUJIRO; SUZUKI KAZUNARI; TSUBOSAKI  
KUNIHIO; SUZUKI HIROMICHI; MIYAKI YOSHINORI; NAITO TAKAHIRO; KAWAI  
SUEO

Priority (No,Kind,Date): JP 98185261 A 19980630; JP 9271116 A  
19920327; JP 92320098 A 19921130

Applic (No,Kind,Date): JP 98185261 A 19980630

IPC: \* H01L-023/50

Derwent WPI Acc No: \* C 94-288930; G 99-091870; G 99-091870

JAPIO Reference No: \* 180577E000032

Language of Document: Japanese

Patent (No,Kind,Date): JP 11224928 A2 19990817

SEMICONDUCTOR INTEGRATED CIRCUIT (English)

Patent Assignee: HITACHI LTD; HITACHI ULSI SYS CO LTD

Author (Inventor): KAJIWARA YUJIRO; SUZUKI KAZUNARI; TSUBOSAKI  
KUNIHIO; SUZUKI HIROMICHI; MIYAKI YOSHINORI; NAITO TAKAHIRO; KAWAI  
SUEO

Priority (No,Kind,Date): JP 9271116 A 19920327; JP 92320098 A 19921130  
 Applic (No,Kind,Date): JP 98333269 A 19980101  
 IPC: \* H01L-023/50  
 Derwent WPI Acc No: \* C 94-288930; G 99-091870; G 99-514376; G 99-514377; G 99-514378; G 2001-011512; G 99-514376  
 JAPIO Reference No: \* 180577E000032  
 Language of Document: Japanese  
 Patent (No,Kind,Date): JP 11224929 A2 19990817  
 SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE (English)  
 Patent Assignee: HITACHI LTD; HITACHI ULSI SYS CO LTD  
 Author (Inventor): KAJIWARA YUJIRO; SUZUKI KAZUNARI; TSUBOSAKI KUNIHIO; SUZUKI HIROMICHI; MIYAKI YOSHINORI; NAITO TAKAHIRO; KAWAI SUEO  
 Priority (No,Kind,Date): JP 9271116 A 19920327; JP 92320098 A 19921130  
 Applic (No,Kind,Date): JP 98333270 A 19980101  
 IPC: \* H01L-023/50  
 Derwent WPI Acc No: \* C 94-288930; G 99-091870; G 99-514376; G 99-514377; G 99-514378; G 2001-011512; G 99-514377  
 JAPIO Reference No: \* 180577E000032  
 Language of Document: Japanese  
 Patent (No,Kind,Date): JP 11224930 A2 19990817  
 SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE (English)  
 Patent Assignee: HITACHI LTD; HITACHI ULSI SYS CO LTD  
 Author (Inventor): KAJIWARA YUJIRO; SUZUKI KAZUNARI; TSUBOSAKI KUNIHIO; SUZUKI HIROMICHI; MIYAKI YOSHINORI; NAITO TAKAHIRO; KAWAI SUEO  
 Priority (No,Kind,Date): JP 9271116 A 19920327; JP 92320098 A 19921130  
 Applic (No,Kind,Date): JP 98333271 A 19980101  
 IPC: \* H01L-023/50  
 Derwent WPI Acc No: \* C 94-288930; G 99-091870; G 99-514376; G 99-514377; G 99-514378; G 2001-011512; G 99-514378  
 JAPIO Reference No: \* 180577E000032  
 Language of Document: Japanese  
 Patent (No,Kind,Date): JP 2000286374 A2 20001013  
 Priority (No,Kind,Date): JP 9271116 A 19920327; JP 92320098 A 19921130  
 Applic (No,Kind,Date): JP 20002000073486 A 20000101  
 IPC: \* H01L-023/50  
 Derwent WPI Acc No: ; G 2001-011512  
 Language of Document: Japanese  
 Patent (No,Kind,Date): JP 2891607 B2 19990517  
 Patent Assignee: HITACHI LTD; HITACHI CHO ERU ESU AI SHISUTE  
 Author (Inventor): KAJIWARA YUJIRO; SUZUKI KAZUNARI; TSUBOSAKI KUNIHIO; SUZUKI HIROMICHI; MYAKI YOSHINORI; NAITO TAKAHIRO; KAWAI SUEO  
 Priority (No,Kind,Date): JP 9365784 A 19930325; JP 9271116 A 19920327; JP 92320098 A 19921130  
 Applic (No,Kind,Date): JP 9365784 A 19930325  
 IPC: \* H01L-023/50; H01L-021/60  
 Language of Document: Japanese

UNITED STATES OF AMERICA (US)

Patent (No,Kind,Date): US 5378656 A 19950103  
 LEADFRAME, SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE USING THE SAME, AND METHOD OF AND PROCESS FOR FABRICATING THE SAME (English)  
 Patent Assignee: HITACHI LTD (JP); HITACHI MICROCOMPUTER SYST (JP)  
 Author (Inventor): KAJIHARA YUJIRO (JP); SUZUKI KAZUNARI (JP);

TSUBOSAKI KUNIHIRO (JP); SUZUKI HIROMICHI (JP); MIYAKI YOSHINORI  
 (JP); NAITO TAKAHIRO (JP); KAWAI SUEO (JP)  
 Priority (No,Kind,Date): JP 9271116 A 19920327; JP 92320098 A  
 19921130  
 Applic (No,Kind,Date): US 38684 A 19930329  
 National Class: \* 437217000; 437209000; 437211000; 437214000;  
 437220000  
 IPC: \* H01L-021/60  
 Derwent WPI Acc No: \* C 94-288930  
 JAPIO Reference No: \* 180577E000032  
 Language of Document: English  
 Patent (No,Kind,Date): US 5637913 A 19970610  
 LEADFRAME SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE USING THE SAME AND  
 METHOD OF AND PROCESS FOR FABRICATING THE TWO (English)  
 Patent Assignee: HITACHI LTD (JP); HITACHI MICROCOMPUTER SYST (JP)  
 Author (Inventor): KAJIHARA YUJIRO (JP); SUZUKI KAZUNARI (JP);  
 TSUBOSAKI KUNIHIRO (JP); SUZUKI HIROMICHI (JP); MIYAKI YOSHINORI  
 (JP); NAITO TAKAHIRO (JP); KAWAI SUEO (JP)  
 Priority (No,Kind,Date): US 311021 A 19940922; JP 9271116 A  
 19920327; JP 92320098 A 19921130; US 38684 A3 19930329  
 Applic (No,Kind,Date): US 311021 A 19940922  
 National Class: \* 257666000; 257672000; 257674000; 257676000;  
 257783000  
 IPC: \* H01L-023/495; H01L-023/48  
 Derwent WPI Acc No: \* C 94-288930  
 JAPIO Reference No: \* 180577E000032  
 Language of Document: English

UNITED STATES OF AMERICA (US)

Legal Status (No,Type,Date,Code,Text):

US 5378656	P	19920327	US AA	PRIORITY (PATENT)
		JP 9271116	A	19920327
US 5378656	P	19921130	US AA	PRIORITY (PATENT)
		JP 92320098	A	19921130
US 5378656	P	19930329	US AE	APPLICATION DATA (PATENT)
		(APPL. DATA (PATENT))		
		US 38684	A	19930329
US 5378656	P	19940721	US AS02	ASSIGNMENT OF ASSIGNOR'S INTEREST HITACHI, LTD. CHIYODA-KU, TOKYO, JAPAN; HITACHI MICROCOMPUTER SYSTEM LTD. KODAIR ; KAJIHARA, YUJIRO : 19930304; SUZUKI, KAZUNARI : 19930304; TSUBOSAKI, KUNIHIRO : 19930304; SUZUKI, HIROMICHI : 19930304;
US 5378656	P	19950103	US A	PATENT
US 5637913	P	19920327	US AA	PRIORITY (PATENT)
		JP 9271116	A	19920327
US 5637913	P	19921130	US AA	PRIORITY (PATENT)
		JP 92320098	A	19921130
US 5637913	P	19930329	US AA	PRIORITY
		US 38684	A3	19930329
US 5637913	P	19940922	US AE	APPLICATION DATA (PATENT)
		(APPL. DATA (PATENT))		
		US 311021	A	19940922
US 5637913	P	19970610	US A	PATENT
US 5637913	P	19990803	US RF	REISSUE APPLICATION FILED (REISSUE APPL. FILED) 19990609

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**Pat. No. 5637913, \*****5,637,913****♦ GET 1st DRAWING SHEET OF 21****Jun. 10, 1997****Leadframe semiconductor integrated circuit device using the same and method of and process for fabricating the two****REISSUE:** Reissue Application filed Jun. 9, 1999 (O.G. Aug. 3, 1999) Ex. Gp.: 2811; Re. S.N. 09/328,910**INVENTOR:** Kajihara, Yujiro, Tachikawa, Japan  
Suzuki, Kazunari, Tokyo, Japan  
Tsubosaki, Kunihiro, Hino, Japan  
Suzuki, Hiromichi, Machida, Japan  
Miyaki, Yoshinori, Kokyubunji, Japan  
Naito, Takahiro, Koganei, Japan  
Kawai, Sueo, Iwama-machi, Japan**ASSIGNEE-AT-ISSUE:** Hitachi, Ltd., Tokyo, Japan (03) , Hitachi Microcomputer System, Ltd., Tokyo, Japan (03)**APPL-NO:** 311,021**FILED:** Sep. 22, 1994**FOR-PRIOR:**Mar. 27, 1992 Japan 4-071116  
Nov. 30, 1992 Japan 4-320098**REL-US-DATA:**

Division of Ser. No. 38,684, Mar. 29, 1993

**INT-CL:** [6] H01L 23#495; H01L 23#48**US-CL:** 257#666; 257#672; 257#674; 257#676; 257#783;**CL:** 257;**SEARCH-FLD:** 257#676, 666, 672, 677, 670, 674, 675, 669, 783, 784, 785, 782**REF-CITED:****U.S. PATENT DOCUMENTS**


4,534,105	8/1985 *	Reusch	29#589
4,663,650	5/1987 *	Gilder, Jr. et al.	257#676
4,829,362	5/1989 *	Tran et al.	437#217
4,835,120	5/1989 *	Mallik et al.	437#207
4,868,635	9/1989 *	Frechette et al.	257#670
4,989,068	1/1991 *	Yasuhara et al.	257#677

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**Questel-Orbit:**

I/I LGST - (C) LEGSTAT  
PN - US 5637913 [US5637913]  
AP - US 311021/94 19940922 [1994US-0311021]  
DT - US-P  
ACT - 19940922 US/AE-A  
APPLICATION DATA (PATENT)  
{US 311021/94 19940922 [1994US-0311021]}  
- 19970610 US/A  
PATENT  
- 19990803 US/RF  
REISSUE APPLICATION FILED  
19990609  
UP - 1999-31

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I/I PAST - (C) PAST  
AN - 199931-000575  
PN - 5637913 A [US5637913]  
DT - A (UTILITY)  
OG - 1999-08-03  
CO - REA  
ACT - REISSUE APPLICATION FILED  
SH - REISSUE APPLICATION FILED

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I/I CRXX - (C) CLAIMS/RRX  
AN - 2850172  
PN - 5,637,913 A 19970610 [US5637913]  
PT - E (Electrical)  
PA - Hitachi Ltd JP; Hitachi Microcomputer System Ltd JP  
ACT - 19990609 REISSUE REQUESTED  
Issue Date of O.G.: 19990803  
Reissue Request Number: 09/328910  
Examination Group responsible for Reissue process: 2811  
  
- 20000518 REASSIGNED  
CHANGE OF NAME

Assignor: HITACHI MICROCOMPUTER SYSTEM, LTD. DATE SIGNED: 04/01/1998

Assignee: HITACHI ULSI SYSTEMS CO., LTD. 22-1, JOSUIHONCHO, 5-CHOME,  
KODAIRA-SHI TOKYO, JAPAN

Reel 010828/Frame 0905

Contact: ANTONELLI, TERRY, STOUT & KRAUS, LLP PAUL J. SKWIERAWSKI 1300  
N. SEVENTEENTH STREET, SUITE 1800 ARLINGTON, VA 22209

UP - 1999-30  
UACT- 1999-08-03  
URAS- 2000-12-26